

# Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

B.Tech in Electronics and Communication - IOT

III - SEMESTER

| Sr. No. | Subject Code | Name of Subject                            | Course Code | Teaching Scheme/ Week |   |    | Examination Scheme      |               |                      |                         |                      | Total Marks | Credits |
|---------|--------------|--|-------------|-----------------------|---|----|-------------------------|---------------|----------------------|-------------------------|----------------------|-------------|---------|
|         |              |  |             | L                     | T | P  | Theory                  |               |                      | Practical               |                      |             |         |
|         |              |  |             |                       |   |    | End Sem University Exam | Two Term Exam | Teachers Asses-ment* | End Sem University Exam | Teachers Asses-ment* |             |         |
| 1       | BTMA301      | Applied Mathematics-III                    |             | 3                     | 1 | 0  | 60                      | 20            | 20                   | 0                       | 0                    | 100         | 4       |
| 2       | BTEC302      | Network Analysis & Synthesis               |             | 3                     | 1 | 2  | 60                      | 20            | 20                   | 30                      | 20                   | 150         | 5       |
| 3       | BTEC303      | Electronic Measurement and Instrumentation |             | 3                     | 1 | 2  | 60                      | 20            | 20                   | 30                      | 20                   | 150         | 5       |
| 4       | BTEC305      | Electronic Circuits                        |             | 3                     | 1 | 2  | 60                      | 20            | 20                   | 30                      | 20                   | 150         | 5       |
| 5       | BTECIOT301   | Introduction to IOT & Embedded Systems     |             | 3                     | 1 | 0  | 60                      | 20            | 20                   | 0                       | 0                    | 100         | 4       |
| 6       | BTEC306      | Personality Development-1                  |             | 0                     | 0 | 2  | 0                       | 0             | 0                    | 0                       | 50                   | 50          | 1       |
| 7       | BTEC307      | Software Lab-1 (PCB Designing)             |             | 0                     | 0 | 4  | 0                       | 0             | 0                    | 50                      | 50                   | 100         | 2       |
| TOTAL   |              |  |             | 15                    | 5 | 12 | 300                     | 100           | 100                  | 140                     | 160                  | 800         | 26      |

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

  
Chairperson  
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya  
Indore

  
Joint Registrar  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore